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REPLY UNDER 37 C.F.R § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 3629

PATENT
5298-04500/PM00026

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Li et al.

Serial No. 09/849,047

Filed: May 4, 2001

For: BURIED LAYER SUBSTRATE
ISOLATION IN INTEGRATED
CIRCUITS

§ Group Art Unit: 2822
§ Examiner: Lewis, M.
§ Atty. Dkt. No. 5298-04500

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Pamela Gerik
Pamela Gerik

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RESPONSE AFTER FINAL REJECTION PURSUANT TO 37 CFR § 1.116

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Washington, D.C. 20231

Dear Sir/ Madam:

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This paper is submitted in response to the final Office Action dated July 23, 2003 to further
highlight reasons why the application is in condition for allowance.

Remarks begin on page 2 of this paper.